PGIS-222 A-21

M.Sc. I Semester (CBCS) Degree Examination **ELECTRONICS AND INSTRUMENTATION**

Analog and Digital Electronics

Paper: HCT 1.1

Time: 3 Hours

Maximum Marks: 80

Instructions to Candidates:

- Answer the question as per the instructions. 1)
- Write the question numbers clearly. 2)

PART-A

Answer any Eight of the following:

 $(8 \times 2 = 16)$

- What is rectifier? What is the value of ripple factor for half-wave rectifier? 1. a)
 - Draw the block diagram of regulated DC power supply. b)
 - What is filter? Draw the diagram of RC filter. c)
 - Define slew rate and input bias currents of op amp. d)
 - Draw the diagram of current mirror. e)
 - Calculate the CMRR of op amp. If Ad is 10⁴ and Ac is 0.1 f)
 - Convert 105.75₁₀ to binary. g)
 - State De Morgan's theorems. h)
 - Draw the diagram of discrete two input AND gate. i)
 - What are the advantages of synchronous counters? j)

PART-B

Answer any Four of the following:

 $(4 \times 7 = 28)$

- Discuss the characteristics of ideal and practical op amps. 2.
- Explain the design and working of discrete component voltage regulator with diagram. 3.
- With diagram discuss working of instrumentation amplifier. Derive its output equation. 4.
- With neat diagrams explain working of Full-Adder and Full-Subtractor. 5.
- Discuss the design and working of Mod-10 counter with necessary diagrams. 6.
- With neat sketch explain working of serial-in parallel-out shift register. 7.

Contd....

PART-C

Answer any Three of the following:

 $(3 \times 12 = 36)$

- **8.** With neat sketch, explain the principle and working of SMPS.
- 9. Discuss various configurations of op amp. With diagrams.
- **10.** With diagrams, discuss design and working of Excess-3 Adder and subtractor using 4-bit binary adders.
- 11. Discuss the design and working of 3-bit up/down counter with diagrams.
- 12. Write short notes on any **Two** of the following.

 $(2 \times 6 = 12)$

- a) 723 regulator.
- b) Wein-bridge oscillator.
- c) Flip-flops.
- d) Parallel-in serial-out shift register.

Roll No

PGIS-225 A-21

M.Sc. I Semester Degree Examination ELECTRONICS AND INSTRUMENTATION

Introduction to 8086 Microprocessors and 'C' Programming

Paper: SCT 1.1

Time: 3 Hours

Maximum Marks: 80

Instructions to Candidates:

- Write question number clearly. i)
- ii) Draw a neat labelled **diagrams** wherever necessary.

PART-A

Answer any Eight of the following:

- 1. Name the functional units of 8086 CPU.
 - b) What do you mean by LAHF and SAHF instructions?
 - c) Write the purpose of program segments.
 - What is the necessity of interfacing? d)
 - Sketch Modem handshaking diagram. e)
 - Write the main purpose of 8259. f)
 - Define an interrupt. Write its types. g)
 - h) Define a pointer and its significance.
 - Write a 'C' program to multiply elements of two arrays. i)
 - What is the advantage of program control statements? i)

PART-B

Answer any Four of the following:

 $(4 \times 7 = 28)$

- Explain the data addressing modes of 8086 microprocessor. 2.
- Discuss the salient features of ALP development tools. 3.
- 4. Explain the programmable peripheral IC 8279 configuration.
- Describe the application of microprocessor for controlling the speed of stepper motor. 5.
- With suitable examples, explain the applications of arrays. 6.
- How will you represent data types in C programming? Explain with examples. 7.

PGIS-225 A-21/2021

(1)

[Contd....

PART-C

Answer any Three of the following:

 $(3 \times 12 = 36)$

- 8. With a neat block diagram, explain the architecture of 8086.
- 9. With a neat diagram and suitable example, explain timing diagram parameters.
- 10. Write an overview on serial data transmission methods.
- 11. Discuss various functions and ways of passing values to C functions.
- 12. Write short notes on any Two of the following.

 $(2 \times 6 = 12)$

- a) MACROS.
- b) Modes of 8254.
- c) RS 232 Interface.
- d) Loop statements in C Language.